

**Table 2-4: P6810 Probe signal connections on 136 and 102 channel modules for probe #4 and probe # 3**

		136 channel module				102 channel module			
		Probe #4				Probe #3			
Signal name	Podlet	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station
Clk/Qual	Clk/Qual	Q3-		Q2-		CK0-		Q0-	
Clk/Qual+	Clk/Qual	Q3+		Q2+		CK0+		Q0+	
Data 7-	7	E3:7-	E2:7-	E1:7-	E0:7-	A3:7-	A2:7-	D3:7-	D2:7-
Data 7+	7	E3:7+	E2:7+	E1:7+	E0:7+	A3:7+	A2:7+	D3:7+	D2:7+
Data 6-	6	E3:6-	E2:6-	E1:6-	E0:6-	A3:6-	A2:6-	D3:6-	D2:6-
Data 6+	6	E3:6+	E2:6+	E1:6+	E0:6+	A3:6+	A2:6+	D3:6+	D2:6+
Data 5-	5	E3:5-	E2:5-	E1:5-	E0:5-	A3:5-	A2:5-	D3:5-	D2:5-
Data 5+	5	E3:5+	E2:5+	E1:5+	E0:5+	A3:5+	A2:5+	D3:5+	D2:5+
Data 4-	4	E3:4-	E2:4-	E1:4-	E0:4-	A3:4-	A2:4-	D3:4-	D2:4-
Data 4+	4	E3:4+	E2:4+	E1:4+	E0:4+	A3:4+	A2:4+	D3:4+	D2:4+
Data 3-	3	E3:3-	E2:3-	E1:3-	E0:3-	A3:3-	A2:3-	D3:3-	D2:3-
Data 3+	3	E3:3+	E2:3+	E1:3+	E0:3+	A3:3+	A2:3+	D3:3+	D2:3+
Data 2-	2	E3:2-	E2:2-	E1:2-	E0:2-	A3:2-	A2:2-	D3:2-	D2:2-
Data 2+	2	E3:2+	E2:2+	E1:2+	E0:2+	A3:2+	A2:2+	D3:2+	D2:2+
Data 1-	1	E3:1-	E2:1-	E1:1-	E0:1-	A3:1-	A2:1-	D3:1-	D2:1-
Data 1+	1	E3:1+	E2:1+	E1:1+	E0:1+	A3:1+	A2:1+	D3:1+	D2:1+
Data 0-	0	E3:0-	E2:0-	E1:0-	E0:0-	A3:0-	A2:0-	D3:0-	D2:0-
Data 0+	0	E3:0+	E2:0+	E1:0+	E0:0+	A3:0+	A2:0+	D3:0+	D2:0+

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2- 5: Probe signal connections on 102 and 136 channel modules for probe #2 and probe #1**

		Probe #2				Probe #1			
Signal name	Podlet	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station
Clk/Qual-	Clk/Qual-	CK1-		CK2-		CK3-		Q1-	
Clk/Qual+	Clk/Qual+	CK1+		CK2+		CK3+		Q1+	
Data 7-	7	A1:7-	A0:7-	D1:7-	D0:7-	C3:7-	C2:7-	C1:7-	C0:7-
Data 7+	7	A1:7+	A0:7+	D1:7+	D0:7+	C3:7+	C2:7+	C1:7+	C0:7+
Data 6-	6	A1:6-	A0:6-	D1:6-	D0:6-	C3:6-	C2:6-	C1:6-	C0:6-
Data 6+	6	A1:6+	A0:6+	D1:6+	D0:6+	C3:6+	C2:6+	C1:6+	C0:6+
Data 5-	5	A1:5-	A0:5-	D1:5-	D0:5-	C3:5-	C2:5-	C1:5-	C0:5-
Data 5+	5	A1:5+	A0:5+	D1:5+	D0:5+	C3:5+	C2:5+	C1:5+	C0:5+
Data 4-	4	A1:4-	A0:4-	D1:4-	D0:4-	C3:4-	C2:4-	C1:4-	C0:4-
Data 4+	4	A1:4+	A0:4+	D1:4+	D0:4+	C3:4+	C2:4+	C1:4+	C0:4+
Data 2+	2	A1:2+	A0:2+	D1:2+	D0:2+	C3:2+	C2:2+	C1:2+	C0:2+
Data 3-	3	A1:3-	A0:3-	D1:3-	D0:3-	C3:3-	C2:3-	C1:3-	C0:3-
Data 3+	3	A1:3+	A0:3+	D1:3+	D0:3+	C3:3+	C2:3+	C1:3+	C0:3+
Data 2-	2	A1:2-	A0:2-	D1:2-	D0:2-	C3:2-	C2:2-	C1:2-	C0:2-
Data 1-	1	A1:1-	A0:1-	D1:1-	D0:1-	C3:1-	C2:1-	C1:1-	C0:1-
Data 1+	1	A1:1+	A0:1+	D1:1+	D0:1+	C3:1+	C2:1+	C1:1+	C0:1+
Data 0-	0	A1:0-	A0:0-	D1:0-	D0:0-	C3:0-	C2:0-	C1:0-	C0:0-
Data 0+	0	A1:0+	A0:0+	D1:0+	D0:0+	C3:0+	C2:0+	C1:0+	C0:0+

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2-6: P6810 Probe signal connections on 68 and 34 channel modules**

		68 channel module				34 channel module			
		Probe #2				Probe #1			
Signal name	Podlet	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station	Way Station
Clk/Qual	Clk/Qual	CK1-		CK2-		CK3-		CK0-	
Clk/Qual+	Clk/Qual	CK1+		CK2+		CK3+		CK0+	
Data 7-	7	A1:7-	A0:7-	D1:7-	D0:7-	C3:7-	C2:7-	A3:7-	A2:7-
Data 7+	7	A1:7+	A0:7+	D1:7+	D0:7+	C3:7+	C2:7+	A3:7+	A2:7+
Data 6-	6	A1:6-	A0:6-	D1:6-	D0:6-	C3:6-	C2:6-	A3:6-	A2:6-
Data 6+	6	A1:6+	A0:6+	D1:6+	D0:6+	C3:6+	C2:6+	A3:6+	A2:6+
Data 5-	5	A1:5-	A0:5-	D1:5-	D0:5-	C3:5-	C2:5-	A3:5-	A2:5-
Data 5+	5	A1:5+	A0:5+	D1:5+	D0:5+	C3:5+	C2:5+	A3:5+	A2:5+
Data 4-	4	A1:4-	A0:4-	D1:4-	D0:4-	C3:4-	C2:4-	A3:4-	A2:4-
Data 4+	4	A1:4+	A0:4+	D1:4+	D0:4+	C3:4+	C2:4+	A3:4+	A2:4+
Data 3-	3	A1:3-	A0:3-	D1:3-	D0:3-	C3:3-	C2:3-	A3:3-	A2:3-
Data 3+	3	A1:3+	A0:3+	D1:3+	D0:3+	C3:3+	C2:3+	A3:3+	A2:3+
Data 2-	2	A1:2-	A0:2-	D1:2-	D0:2-	C3:2-	C2:2-	A3:2-	A2:2-
Data 2+	2	A1:2+	A0:2+	D1:2+	D0:2+	C3:2+	C2:2+	A3:2+	A2:2+
Data 1-	1	A1:1-	A0:1-	D1:1-	D0:1-	C3:1-	C2:1-	A3:1-	A2:1-
Data 1+	1	A1:1+	A0:1+	D1:1+	D0:1+	C3:1+	C2:1+	A3:1+	A2:1+
Data 0-	0	A1:0-	A0:0-	D1:0-	D0:0-	C3:0-	C2:0-	A3:0-	A2:0-
Data 0+	0	A1:0+	A0:0+	D1:0+	D0:0+	C3:0+	C2:0+	A3:0+	A2:0+

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2-7: P6860 High-Density Probe channel mapping on 136 and 102 channel modules**

		136 channel module							
						102 channel module			
		Probe #4		Probe #3		Probe #2		Probe #1	
Signal name	Pad name	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head
Clk/Qual-	A15	Q3-	Q2-	CK0-	Q0-	CK1-	CK2-	CK3-	Q1-
GND	A14	GND	GND	GND	GND	GND	GND	GND	GND
Clk/Qual+	A13	Q3+	Q2+	CK0+	Q0+	CK1+	CK2+	CK3+	Q1+
Data 15	B12	E3:7	E1:7	A3:7	D3:7	A1:7	D1:7	C3:7	C1:7
GND	B11	GND	GND	GND	GND	GND	GND	GND	GND
Data 14	B10	E3:6	E1:6	A3:6	D3:6	A1:6	D1:6	C3:6	C1:6
Data 13	A12	E3:5	E1:5	A3:5	D3:5	A1:5	D1:5	C3:5	C1:5
GND	A11	GND	GND	GND	GND	GND	GND	GND	GND
Data 12	A10	E3:4	E1:4	A3:4	D3:4	A1:4	D1:4	C3:4	C1:4
Data 11	B9	E3:3	E1:3	A3:3	D3:3	A1:3	D1:3	C3:3	C1:3
GND	B8	GND	GND	GND	GND	GND	GND	GND	GND
Data 10	B7	E3:2	E1:2	A3:2	D3:2	A1:2	D1:2	C3:2	C1:2
Data 9	A9	E3:1	E1:1	A3:1	D3:1	A1:1	D1:1	C3:1	C1:1
GND	A8	GND	GND	GND	GND	GND	GND	GND	GND
Data 8	A7	E3:0	E1:0	A3:0	D3:0	A1:0	D1:0	C3:0	C1:0
Data 7	B6	E2:7	E0:7	A2:7	D2:7	A0:7	D0:7	C2:7	C0:7
GND	B5	GND	GND	GND	GND	GND	GND	GND	GND
Data 6	B4	E2:6	E0:6	A2:6	D2:6	A0:6	D0:6	C2:6	C0:6
Data 5	A6	E2:5	E0:5	A2:5	D2:5	A0:5	D0:5	C2:5	C0:5
GND	A5	GND	GND	GND	GND	GND	GND	GND	GND
Data 4	A4	E2:4	E0:4	A2:4	D2:4	A0:4	D0:4	C2:4	C0:4
Data 3	B3	E2:3	E0:3	A2:3	D2:3	A0:3	D0:3	C2:3	C0:3
GND	B2	GND	GND	GND	GND	GND	GND	GND	GND
Data 2	B1	E2:2	E0:2	A2:2	D2:2	A0:2	D0:2	C2:2	C0:2
Data 1	A3	E2:1	E0:1	A2:1	D2:1	A0:1	D0:1	C2:1	C0:1
GND	A2	GND	GND	GND	GND	GND	GND	GND	GND
Data 0	A1	E2:0	E0:0	A2:0	D2:0	A0:0	D0:0	C2:0	C0:0

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2-8: P6860 High-Density Probe channel mapping on 68 and 34 channel modules**

		68 channel module			
				34 channel module	
		Probe #2		Probe #1	
Signal name	Pad name	Probe head	Probe head	Probe head	Probe head
Clk/Qual-	A15	CK1-	CK2-	CK3-	CK0-
GND	A14	GND	GND	GND	GND
Clk/Qual+	A13	CK1	CK2	CK3	CK0
Data 15	B12	A1:7	D1:7	C3:7	A3:7
GND	B11	GND	GND	GND	GND
Data 14	B10	A1:6	D1:6	C3:6	A3:6
Data 13	A12	A1:5	D1:5	C3:5	A3:5
GND	A11	GND	GND	GND	GND
Data 12	A10	A1:4	D1:4	C3:4	A3:4
Data 11	B9	A1:3	D1:3	C3:3	A3:3
GND	B8	GND	GND	GND	GND
Data 10	B7	A1:2	D1:2	C3:2	A3:2
Data 9	A9	A1:1	D1:1	C3:1	A3:1
GND	A8	GND	GND	GND	GND
Data 8	A7	A1:0	D1:0	C3:0	A3:0
Data 7	B6	A0:7	D0:7	C2:7	A2:7
GND	B5	GND	GND	GND	GND
Data 6	B4	A0:6	D0:6	C2:6	A2:6
Data 5	A6	A0:5	D0:5	C2:5	A2:5
GND	A5	GND	GND	GND	GND
Data 4	A4	A0:4	D0:4	C2:4	A2:4
Data 3	B3	A0:3	D0:3	C2:3	A2:3
GND	B2	GND	GND	GND	GND
Data 2	B1	A0:2	D0:2	C2:2	A2:2
GND	B2	GND	GND	GND	GND
GND	B2	GND	GND	GND	GND
Data 2	B1	A0:2	D0:2	C2:2	A2:2
Data 1	A3	A0:1	D0:1	C2:1	A2:1
GND	A2	GND	GND	GND	GND
Data 0	A1	A0:0	D0:0	C2:0	A2:0

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2-9: P6880 High-Density Differential Probe channel mapping on 136 and 102 channel modules for probe #4 and probe #3**

		136 channel module				102 channel module			
		Probe #4				Probe #3			
Signal name	Pad name	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head
Clk/Qual-	A15	Q3-		Q2-		CK0-		Q0-	
GND	A14	GND	GND	GND	GND	GND	GND	GND	GND
Clk/Qual+	A13	Q3+		Q2+		CK0+		Q0+	
Data 7+	B12	E3:7+	E2:7+	E1:7+	E0:7+	A3:7+	A2:7+	D3:7+	D2:7+
GND	B11	GND	GND	GND	GND	GND	GND	GND	GND
Data 7-	B10	E3:7-	E2:7-	E1:7-	E0:7-	A3:7-	A2:7-	D3:7-	D2:7-
Data 6-	A12	E3:6-	E2:6-	E1:6-	E0:6-	A3:6-	A2:6-	D3:6-	D2:6-
GND	A11	GND	GND	GND	GND	GND	GND	GND	GND
Data 6+	A10	E3:6+	E2:6+	E1:6+	E0:6+	A3:6+	A2:6+	D3:6+	D2:6+
Data 5+	B9	E3:5+	E2:5+	E1:5+	E0:5+	A3:5+	A2:5+	D3:5+	D2:5+
GND	B8	GND	GND	GND	GND	GND	GND	GND	GND
Data 5-	B7	E3:5-	E2:5-	E1:5-	E0:5-	A3:5-	A2:5-	D3:5-	D2:5-
Data 4-	A9	E3:4-	E2:4-	E1:4-	E0:4-	A3:4-	A2:4-	D3:4-	D2:4-
GND	A8	GND	GND	GND	GND	GND	GND	GND	GND
Data 4+	A7	E3:4+	E2:4+	E1:4+	E0:4+	A3:4+	A2:4+	D3:4+	D2:4+
Data 3+	B6	E3:3+	E2:3+	E1:3+	E0:3+	A3:3+	A2:3+	D3:3+	D2:3+
GND	B5	GND	GND	GND	GND	GND	GND	GND	GND
Data 3-	B4	E3:3-	E2:3-	E1:3-	E0:3-	A3:3-	A2:3-	D3:3-	D2:3-
Data 2-	A6	E3:2-	E2:2-	E1:2-	E0:2-	A3:2-	A2:2-	D3:2-	D2:2-
GND	A5	GND	GND	GND	GND	GND	GND	GND	GND
Data 2+	A4	E3:2+	E2:2+	E1:2+	E0:2+	A3:2+	A2:2+	D3:2+	D2:2+
Data 1+	B3	E3:1+	E2:1+	E1:1+	E0:1+	A3:1+	A2:1+	D3:1+	D2:1+
GND	B2	GND	GND	GND	GND	GND	GND	GND	GND
Data 1-	B1	E3:1-	E2:1-	E1:1-	E0:1-	A3:1-	A2:1-	D3:1-	D2:1-
Data 0-	A3	E3:0-	E2:0-	E1:0-	E0:0-	A3:0-	A2:0-	D3:0-	D2:0-
GND	A2	GND	GND	GND	GND	GND	GND	GND	GND
Data 0+	A1	E3:0+	E2:0+	E1:0+	E0:0+	A3:0+	A2:0+	D3:0+	D2:0+

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2- 10: P6880 High-Density Differential Probe channel mapping on 136 and 102 channel modules for probe #2 and probe #1**

		Probe #2				Probe #1			
Signal name	Pad name	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head
Clk/Qual-	A15	CK1-		CK2-		CK3-		Q1-	
GND	A14	GND	GND	GND	GND	GND	GND	GND	GND
Clk/Qual+	A13	CK1-		CK2-		CK3+		Q1+	
Data 7+	B12	A1:7+	A0:7+	D1:7+	D0:7+	C3:7+	C2:7+	C1:7+	C0:7+
GND	B11	GND	GND	GND	GND	GND	GND	GND	GND
Data 7-	B10	A1:7-	A0:7-	D1:7-	D0:7-	C3:7-	C2:7-	C1:7-	C0:7-
Data 6-	A12	A1:6-	A0:6-	D1:6-	D0:6-	C3:6-	C2:6-	C2:6-	C0:6-
GND	A11	GND	GND	GND	GND	GND	GND	GND	GND
Data 6+	A10	A1:6+	A0:6+	D1:6+	D0:6+	C3:6+	C2:6+	C1:6+	C0:6+
Data 5+	B9	A1:5+	A0:5+	D1:5+	D0:5+	C3:5+	C2:5+	C1:5+	C0:5+
GND	B8	GND	GND	GND	GND	GND	GND	GND	GND
Data 5-	B7	A1:5-	A0:5-	D1:5-	D0:5-	C3:5-	C2:5-	C1:5-	C0:5-
Data 4-	A9	A1:4-	A0:4-	D1:4-	D0:4-	C3:4-	C2:4-	C1:4-	C0:4-
GND	A8	GND	GND	GND	GND	GND	GND	GND	GND
Data 4+	A7	A1:4+	A0:4+	D1:4+	D0:4+	C3:4+	C2:4+	C1:4+	C0:4+
Data 3+	B6	A1:3+	A0:3+	D1:3+	D0:3+	C3:3+	C2:3+	C1:3+	C0:3+
GND	B5	GND	GND	GND	GND	GND	GND	GND	GND
Data 3-	B4	A1:3-	A0:3-	D1:3-	D0:3-	C3:3-	C2:3-	C1:3-	C0:3-
Data 2-	A6	A1:2-	A0:2-	D1:2-	D0:2-	C3:2-	C2:2-	C1:2-	C0:2-
GND	A5	GND	GND	GND	GND	GND	GND	GND	GND
Data 2+	A4	A1:2+	A0:2+	D1:2+	D0:2+	C3:2+	C2:2+	C1:2+	C0:2+
Data 1+	B3	A1:1+	A0:1+	D1:1+	D0:1+	C3:1+	C2:1+	C1:1+	C0:1+
GND	B2	GND	GND	GND	GND	GND	GND	GND	GND
Data 1-	B1	A1:1-	A0:1-	D1:1-	D0:1-	C3:1-	C2:1-	C1:1-	C0:1-
Data 0-	A3	A1:0-	A0:0-	D1:0-	D0:0-	C3:0-	C2:0-	C1:0-	C0:0-
GND	A2	GND	GND	GND	GND	GND	GND	GND	GND
Data 0+	A1	A1:0+	A0:0+	D1:0+	D0:0+	C3:0+	C2:0+	C1:0+	C0:0+

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2- 11: P6880 High-Density Differential Probe channel mapping on 68 and 34 channel modules**

		68 channel module				34 channel module			
		Probe #2				Probe #1			
Signal name	Pad name	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head	Probe head
Clk/Qual-	A15	CK1-		CK2-		CK3-		CK0-	
GND	A14	GND	GND	GND	GND	GND	GND	GND	GND
Clk/Qual+	A13	CK1-		CK2-		CK3+		CK0+	
Data 7+	B12	A1:7+	A0:7+	D1:7+	D0:7+	C3:7+	C2:7+	A3:7+	A2:7+
GND	B11	GND	GND	GND	GND	GND	GND	GND	GND
Data 7-	B10	A1:7-	A0:7-	D1:7-	D0:7-	C3:7-	C2:7-	A3:7-	A2:7-
Data 6-	A12	A1:6-	A0:6-	D1:6-	D0:6-	C3:6-	C2:6-	A3:6-	A2:6-
GND	A11	GND	GND	GND	GND	GND	GND	GND	GND
Data 6+	A10	A1:6+	A0:6+	D1:6+	D0:6+	C3:6+	C2:6+	A3:6+	A2:6+
Data 5+	B9	A1:5+	A0:5+	D1:5+	D0:5+	C3:5+	C2:5+	A3:5+	A2:5+
GND	B8	GND	GND	GND	GND	GND	GND	GND	GND
Data 5-	B7	A1:5-	A0:5-	D1:5-	D0:5-	C3:5-	C2:5-	A3:5-	A2:5-
Data 4-	A9	A1:4-	A0:4-	D1:4-	D0:4-	C3:4-	C2:4-	A3:4-	A2:4-
GND	A8	GND	GND	GND	GND	GND	GND	GND	GND
Data 4+	A7	A1:4+	A0:4+	D1:4+	D0:4+	C3:4+	C2:4+	A3:4+	A2:4+
Data 3+	B6	A1:3+	A0:3+	D1:3+	D0:3+	C3:3+	C2:3+	A3:3+	A2:3+
GND	B5	GND	GND	GND	GND	GND	GND	GND	GND
Data 3-	B4	A1:3-	A0:3-	D1:3-	D0:3-	C3:3-	C2:3-	A3:3-	A2:3-
Data 2-	A6	A1:2-	A0:2-	D1:2-	D0:2-	C3:2-	C2:2-	A3:2-	A2:2-
GND	A5	GND	GND	GND	GND	GND	GND	GND	GND
Data 2+	A4	A1:2+	A0:2+	D1:2+	D0:2+	C3:2+	C2:2+	A3:2+	A2:2+
Data 1+	B3	A1:1+	A0:1+	D1:1+	D0:1+	C3:1+	C2:1+	A3:1+	A2:1+
GND	B2	GND	GND	GND	GND	GND	GND	GND	GND
Data 1-	B1	A1:1-	A0:1-	D1:1-	D0:1-	C3:1-	C2:1-	A3:1-	A2:1-
Data 0-	A3	A1:0-	A0:0-	D1:0-	D0:0-	C3:0-	C2:0-	A3:0-	A2:0-
GND	A2	GND	GND	GND	GND	GND	GND	GND	GND
Data 0+	A1	A1:0+	A0:0+	D1:0+	D0:0+	C3:0+	C2:0+	A3:0+	A2:0+

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.



**Table 2-12: Recommended Compression-on-PCB to P6434 Mictor adapter and Mictor-on-PCB to Compression adapter channel mapping for 136 and 102 channel modules**

			136 channel module			
			102 channel module			
Signal name	Pad name	Mictor pin number	Adapter #4/ probe #4	Adapter #3/ probe #3	Adapter #2/ probe #2	Adapter #1/ probe #1
J1 Clk/Qual-	A15	NC	NC	NC	NC	NC
GND	A14	GND	GND	GND	GND	GND
J1 Clk/Qual+	A13	5	Q3	Q0	CK0	CK3
J1 Data 15	B12	7	E3:7	D3:7	A3:7	C3:7
GND	B11	GND	GND	GND	GND	GND
J1 Data 14	B10	9	E3:6	D3:6	A3:6	C3:6
J1 Data 13	A12	11	E3:5	D3:5	A3:5	C3:5
GND	A11	GND	GND	GND	GND	GND
J1 Data 12	A10	13	E3:4	D3:4	A3:4	C3:4
J1 Data 11	B9	15	E3:3	D3:3	A3:3	C3:3
GND	B8	GND	GND	GND	GND	GND
J1 Data 10	B7	17	E3:2	D3:2	A3:2	C3:2
J1 Data 9	A9	19	E3:1	D3:1	A3:1	C3:1
GND	A8	GND	GND	GND	GND	GND
J1 Data 8	A7	21	E3:0	D3:0	A3:0	C3:0
J1 Data 7	B6	23	E2:7	D2:7	A2:7	C2:7
GND	B5	GND	GND	GND	GND	GND
J1 Data 6	B4	25	E2:6	D2:6	A2:6	C2:6
J1 Data 5	A6	27	E2:5	D2:5	A2:5	C2:5
GND	A5	GND	GND	GND	GND	GND
J1 Data 4	A4	29	E2:4	D2:4	A2:4	C2:4
J1 Data 3	B3	31	E2:3	D2:3	A2:3	C2:3
GND	B2	GND	GND	GND	GND	GND
J1 Data 2	B1	33	E2:2	D2:2	A2:2	C2:2
J1 Data 1	A3	35	E2:1	D2:1	A2:1	C2:1
GND	A2	GND	GND	GND	GND	GND
J1 Data 0	A1	37	E2:0	D2:0	A2:0	C2:0

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2-12: Recommended Compression-on-PCB to P6434 Mictor adapter and Mictor-on-PCB to Compression adapter channel mapping for 136 and 102 channel modules (Cont.)**

			136 channel module			
			102 channel module			
Signal name	Pad name	Mictor pin number	Adapter #4/ probe #4	Adapter #3/ probe #3	Adapter #2/ probe #2	Adapter #1/ probe #1
J2 Clk/Qual-	A15	NC	NC	NC	NC	NC
GND	A14	GND	GND	GND	GND	GND
J2 Clk/Qual+	A13	6	Q2	CK2	CK1	Q1
J2 Data 15	B12	8	E1:7	D1:7	A1:7	C1:7
GND	B11	GND	GND	GND	GND	GND
J2 Data 14	B10	10	E1:6	D1:6	A1:6	C1:6
J2 Data 13	A12	12	E1:5	D1:5	A1:5	C1:5
GND	A11	GND	GND	GND	GND	GND
J2 Data 12	A10	14	E1:4	D1:4	A1:4	C1:4
J2 Data 11	B9	16	E1:3	D1:3	A1:3	C1:3
GND	B8	GND	GND	GND	GND	GND
J2 Data 10	B7	18	E1:2	D1:2	A1:2	C1:2
J2 Data 9	A9	20	E1:1	D1:1	A1:1	C1:1
GND	A8	GND	GND	GND	GND	GND
J2 Data 8	A7	22	E1:0	D1:0	A1:0	C1:0
J2 Data 7	B6	24	E0:7	D0:7	A0:7	C0:7
GND	B5	GND	GND	GND	GND	GND
J2 Data 6	B4	26	E0:6	D0:6	A0:6	C0:6
J2 Data 5	A6	28	E0:5	D0:5	A0:5	C0:5
GND	A5	GND	GND	GND	GND	GND
J2 Data 4	A4	30	E0:4	D0:4	A0:4	C0:4
J2 Data 3	B3	32	E0:3	D0:3	A0:3	C0:3
GND	B2	GND	GND	GND	GND	GND
J2 Data 2	B1	34	E0:2	D0:2	A0:2	C0:2
J2 Data 1	A3	36	E0:1	D0:1	A0:1	C0:1
GND	A2	GND	GND	GND	GND	GND
J2 Data 0	A1	38	E0:0	D0:0	A0:0	C0:0

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2- 13: Recommended Compression-on-PCB to P6434 Mictor adapter and Mictor-on-PCB to Compression adapter channel mapping for 68 channel module**

Signal name	Pad name	Mictor pin number	Adapter #1/probe #1	Adapter #2/probe #1
J1 Clk/Qual-	A15	NC	NC	NC
GND	A14	GND	GND	GND
J1 Clk/Qual+	A13	5	CK0	CK3
J1 Data 15	B12	7	A3:7	C3:7
GND	B11	GND	GND	GND
J1 Data 14	B10	9	A3:6	C3:6
J1 Data 13	A12	11	A3:5	C3:5
GND	A11	GND	GND	GND
J1 Data 12	A10	13	A3:4	C3:4
J1 Data 11	B9	15	A3:3	C3:3
GND	B8	GND	GND	GND
J1 Data 10	B7	17	A3:2	C3:2
J1 Data 9	A9	19	A3:1	C3:1
GND	A8	GND	GND	GND
J1 Data 8	A7	21	A3:0	C3:0
J1 Data 7	B6	23	A2:7	C2:7
GND	B5	GND	GND	GND
J1 Data 6	B4	25	A2:6	C2:6
J1 Data 5	A6	27	A2:5	C2:5
GND	A5	GND	GND	GND
J1 Data 4	A4	29	A2:4	C2:4
J1 Data 3	B3	31	A2:3	C2:3
GND	B2	GND	GND	GND
J1 Data 2	B1	33	A2:2	C2:2
J1 Data 1	A3	35	A2:1	C2:1
GND	A2	GND	GND	GND
J1 Data 0	A1	37	A2:0	C2:0

1/2 channel mode may use Adapter #1 connection signals as source.

**Table 2-13: Recommended Compression-on-PCB to P6434 Mictor adapter and Mictor-on-PCB to Compression adapter channel mapping for 68 channel modules (Cont.)**

Signal name	Pad name	Mictor pin number	Adapter #1/probe #2	Adapter #2/probe #2
J2 Clk/Qual+	A15	NC	NC	NC
GND	A14	GND	GND	GND
J2 Clk/Qual+	A13	6	CK1	CK2
J2 Data 15	B12	8	A1:7D1:7	D1:7
GND	B11	GND	GND	GND
J2 Data 14	B10	10	A1:6	D1:6
J2 Data 13	A12	12	A1:55	D1:5
GND	A11	GND	GND	GND
J2 Data 12	A10	14	A1:4	D1:4
J2 Data 11	B9	16	A1:3	D1:3
GND	B8	GND	GND	GND
J2 Data 10	B7	18	A1:2	D1:2
J2 Data 9	A9	20	A1:1	D1:1
GND	A8	GND	GND	GND
J2 Data 8	A7	22	A1:0	D1:0
J2 Data 7	B6	24	A0:7	D0:7
GND	B5	GND	GND	GND
J2 Data 6	B4	26	A0:6	D0:6
J2 Data 5	A6	28	A0:5	D0:5
GND	A5	GND	GND	GND
J2 Data 4	A4	30	A0:4	D0:4
J2 Data 3	B3	32	A0:3	D0:3
GND	B2	GND	GND	GND
J2 Data 2	B1	34	A0:2	D0:2
J2 Data 1	A3	36	A0:1	D0:1
GND	A2	GND	GND	GND
J2 Data 0	A1	38	A0:0	D0:0

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2-14: Recommended Compression-on-PCB to P6434 Mictor adapter and Mictor-on-PCB to Compression adapter channel mapping for 34 channel module**

Signal name	Pad name	Mictor pin number	Adapter #1/probe head #1
J1 Clk/Qual-	A15	NC	NC
GND	A14	GND	GND
J1 Clk/Qual+	A13	5	CK3
J1 Data 15	B12	7	C3:7
GND	B11	GND	GND
J1 Data 14	B10	9	C3:6
J1 Data 13	A12	11	C3:5
GND	A11	GND	GND
J1 Data 12	A10	13	C3:4
J1 Data 11	B9	15	C3:3
GND	B8	GND	GND
J1 Data 10	B7	17	C3:2
J1 Data 9	A9	19	C3:1
GND	A8	GND	GND
J1 Data 8	A7	21	C3:0
J1 Data 7	B6	23	C2:7
GND	B5	GND	GND
J1 Data 6	B4	25	C2:6
J1 Data 5	A6	27	C2:5
GND	A5	GND	GND
J1 Data 4	A4	29	C2:4
J1 Data 3	B3	31	C2:3
GND	B2	GND	GND
J1 Data 2	B1	33	C2:2
J1 Data 1	A3	35	C2:1
GND	A2	GND	GND
J1 Data 0	A1	37	C2:0

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.

**Table 2- 14: Recommended Compression-on-PCB to P6434 Mictor adapter and Mictor-on-PCB to Compression adapter channel mapping for 34 channel modules (Cont.)**

Signal name	Pad name	Mictor pin number	Adapter #1/probe head #2
J2 Clk/Qual+	A15	NC	NC
GND	A14	GND	GND
J2 Clk/Qual+	A13	6	CK0
J2 Data 15	B12	8	A3:7
GND	B11	GND	GND
J2 Data 14	B10	10	A3:6
J2 Data 13	A12	12	A3:5
GND	A11	GND	GND
J2 Data 12	A10	14	A3:4
J2 Data 11	B9	16	A3:3
GND	B8	GND	GND
J2 Data 10	B7	18	A3:2
J2 Data 9	A9	20	A3:1
GND	A8	GND	GND
J2 Data 8	A7	22	A3:0
J2 Data 7	B6	24	A2:7
GND	B5	GND	GND
J2 Data 6	B4	26	A2:6
J2 Data 5	A6	28	A2:5
GND	A5	GND	GND
J2 Data 4	A4	30	A2:4
J2 Data 3	B3	32	A2:3
GND	B2	GND	GND
J2 Data 2	B1	34	A2:2
J2 Data 1	A3	36	A2:1
GND	A2	GND	GND
J2 Data 0	A1	38	A2:0

Refer to Table 2-2 on page 2-4 and Table 2-3 on page 2-5 for 2X and 4X demultiplexing channel assignments.

Any differential input, either the differential clock/qualifiers or the P6860 High-density Probe, or all differential data and clock/qualifiers on the P6810 General Purpose Differential Probe and P6880 High-density Differential Probe, may have their negative input pin grounded and be used as a single-ended input.